

Electronic Devices

Honors & Awards

Advance Digital design & Verilog

## Faculty Profile on University Website www.mjpru.ac.in

रूहेलखण्ड विश्वविद्यालयं, बरेली								
Title Mr.	First Name	Varun Pratap	Last Name	Singh		Photograph		
Designation			•		11.11			
Department	Electronics &	Communication						
Address	DEptt. Of ECI	E, FET, MJP						
(Campus	_			100				
(Residence	´ <del>                                    </del>	an Marg Subl	5.8					
Phone No	)  ,							
(Campus								
(Residence	9634218878							
Mobile	9034210070							
Fax								
Email	Varun1select@gmail.com							
Web-Page								
Educational Qualific	ations (Gradu	ation Onwa	ards)					
Course/Degree	Institution		Year		Details/Th	etails/Thesis Topic/Subjects		
B.Tech.	U.P Techni	ical	2009.			s & communication		
	University(							
	APJAKTU)							
	(U.P.)	,,						
M.Tech.	Uttarakhand	1 Technical	2016	2016 D		Digital Communication		
IVI. I CCII.		University Dehradun			Digital Co			
	Offiversity i	Demadun						
Career Profile								
Organization / Institu	ition	Designation	n Durat	tion	Role			
		etronics & Guest Faculty		on 2017		ning.		
Communication	engineering				8-19	, <sub> </sub>		
FET, MJP	Rohilkahnd		sessio	on 2019-				
· ·	1		Session	)II 2019-	20.			
University Bareilly		A	1 St T	ly 2010 t	. O Tanah	.:		
		Institute of Assistant				ning and		
Engineering Bareilly				2017.	Aum	Administrative both		
Dr. APJ Abdul Kala								
University, Lucknov	7 (U.P),							
	+							
Research Interests /	Specialization							
VLSI design	1							
Reversible logic & qua	antum Computa	ation						
Physics & properties								
T. 1: 7	(0.11		1					
Teaching Experience	· •	ourses Taug	(ht)					
Basic Electronics En	gineering							
VLSI Design								
Analog and Digital e	electronics							
Elastronia Darriasa								

Publications /	Publications /Academic Activities (Numbers Only)							
Books &	-	Research Papers	2	Papers Presented	2	Seminars/	Research	
Monographs (Single Author)		Published in International Journals		in Seminars/ Conference		Conferences Organized	Projects (Completed)	
Books (Co- authored)	-	Research Papers Published in Other Journals	-	Seminar/ Conferences Attended	4	Workshops Organized	Research Projects (Ongoing)	
Books (Edited) Chapters in Edited Books	-	Articles Published in Popular Fora, e.g., Websites,	-	Sessions Chaired in Seminars/ Conferences		Membership of Academic/ Professional	Foreign Countries Visited for	
Luited Books		Blogs, Newspapers, Magazines etc.		Resource Lectures Delivered		Bodies	Academic Assignments	

Details of P	ublications /Academic Activities (201	l O Or	wards)				
(a) Books /	(a) Books / Monographs						
Year of Publication	<u>Title</u>		Publisher ISBN			Co-Author (s) (if any)	
1 ublication						(II ally)	
(b) Papers P	(b) Papers Published in Indexed/ Peer Reviewed Journals						
Year of			<u>ISSN</u>		/ Indexing	Co-Author (s)	
<u>Publication</u>						(if any)	
2016	Design of ALU Based on	Int	ernation	e_ICC	N: 2278-	Manish Rai,	
	Reversible Gates	al Journal of Advanced		8875		Ramapati	
	Reversible Gates					Mishra "	
						WIISHI a	
		research in					
		Electrical,					
		Electroni					
		cs and Instrumen					
			on Eng				
2015			ering				
2015	Varun Pratap Singh, , "Efficient		merican		SN: 2320-	Shiv Dayal,	
	carry skip Adder design using	Jou	rnal of		p-ISSN:	Manish Rai	
	full adder and carry skip block	_	gineering	2320	-0936.		
	based on reversible Logic	Res	earch				
		(AJ)	ER)				

## (c) Articles

## (d) Seminar/Conference Presentations

Varun Pratap Singh, Manish Rai, "Reversible Adder-Subtractor Circuit With Carry and Borrow Propagate Facility", proceedings of International Conference on Advances in Computing, Communication & Automation fall (ICACCA 2017), cosponsored by IEEE(Date of Conference: 15-16 Sept. 2017, Electronic ISBN: 978-1-5090-6403-8, Print on Demand ISBN: 978-1-5090-6404-5, INSPEC Accession Number: 17732212

**DOI:** 10.1109/ICACCAF.2017.8344704, **Publisher:** IEEE).

Varun Pratap Singh, Manish Rai, "Verilog Design of Full Adder Based on Basic Reversible
Gates", proceedings of International Conference on Advances in Computing, Communication &
Automation fall (ICACCA 2016), cosponsored by IEEE (Electronic ISBN: 978-1-5090-3480-
2, Print on Demand(PoD) ISBN: 978-1-5090-3481-9 INSPEC Accession. No.
: 16484563, <b>DOI</b> : 10.1109/ ICACCAF.2016. 7748977, Publisher: IEEE).
(e) Resource Lectures Delivered
(f) Seminars/Conferences/Workshops Organized
(g) Public Service / University Service / Consulting Activity
(h) Memberships of Academic/Professional Bodies
Projects (With Title, Year, Grants, Funding Agency and Collaborations)
-
Administrative Positions/Assignments Held
-
Academic Foreign Visits
-
Any Other Details
-

Signature of Faculty Member (in Hard Copy Only)

Note: Please submit this file to E-mail ID: <a href="mailto:rkg@mjpru.ac.in">rkg@mjpru.ac.in</a> through your own E-mail ID.