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FACULTY OF ENGINEERING & TECHNOLOGY

MAHATMA JYOTIBA PHULE ROHILKHAND UNIVERSITY, BAREILLY

Ref. No. FET/MJP/...CSIT/628.....

Date...07/04/2021.....

Notice

This is to inform that the Ph.D. viva-voce examination of **Mr. Lalit Kumar**, Research Scholar, Department of CSIT is scheduled on April-10, 2021. The faculty members, research scholars and students are requested to attend the same. Other details are as given below:

Name of Candidate: Mr. Lalit Kumar

Name of Supervisor: Prof. Vinay Rishiwal, CSIT, M. J. P. R. U.

Thesis Title: Design of Information Hiding based Approach for Public Cloud Data Security

Venue: New Computer Lab, Ground Floor, CSIT Main Building

Time: 11:00 AM

(Dr. Vinay Rishiwal)

Professor, CSIT

Copy to

1. PS to the Hon'ble VC/ Dean FET/Registrar/HoD CSIT